





### Antimonide-based optoelectronic devices epitaxially grown on Silicon

Eric Tournié

### **University of Montpellier (France)**

Institut d'Electronique et des Systèmes (IES), UMR CNRS 5214

eric.tournie@umontpellier.fr

nanomir.edu.umontpellier.fr



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- Antimonide opto devices on Si: motivations
- III-Vs on Si: issues
- GaSb on off-cut Si substrates
  - epitaxial templates
  - GaSb-based laser diodes
  - InAs/AISb quantum-cascade lasers
- GaSb on on-axis Si substrates
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    - InAs/InAsSb type-II superlattice photodetector

- MBE templates
- Summary Perspectives

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### Silicon Photonics R&D and Manufacturing on 300-mm Wafer Platform

F. Boeuf et al. (ST Microelectronics)

JOURNAL OF LIGHTWAVE TECHNOLOGY, VOL. 34, NO. 2, JANUARY 15, 2016



A complete toolbox!

Almost complete: the laser is still missing!

Si and Ge are indirect bandgap materials: no light emission

**Si-photonics needs III-V semiconductor materials** 

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**Si photonics** 



#### Antimonide compound semiconductors

GaSb, AlSb, InSb, InAs and their alloys

• Large bandgap range:

0.1 - 1.8 eV

- Various band alignments: Type I, Type II, Type III
- Large band offsets:  $\Delta E_c = 0 - 2 \text{ eV}$   $\Delta E_v = 0 - 0.5 \text{ eV}$
- Unrivalled band structure engineering

Ismatch with Si ~ 12%

**III-Sbs: perfectly suited for the IR wavelength range** 

### **Sb-based IR devices**



#### The sensing challenge



Mid-IR: atmosphere transparence windows + "fingerprint" region

#### A wealth of applications

Atmospheric pollution monitoring, industrial process control, food industry, health, security, free space optics, etc.

Increasing demand for low-cost, small footprint, smart, photonic sensors.



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#### **III-Sb on Si: experimental**

#### Si substrates

• 4 – 6° offcut (001) 2 inch. Si substrates

#### Standard III-V molecular-beam epitaxy (MBE) reactor

- Solid-source MBE with load-lock chamber
- Valved-cracker cells for As and Sb
- Standard group-III effusion cells

• T<sub>substrate</sub> < 850 °C

• Growth chamber and T<sub>substrate</sub> not compatible with *in-situ* Silicon de-oxidation

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• No Si buffer-layer growth in (most) III-V systems

#### GaSb on Si templates

- 4 6° off (001)Si
- *Ex-situ* O<sub>2</sub> Plasma + HF Si preparation cycles
- *In-situ* annealing at 800°C
- 4 MLs AlSb @ 450°C
- 500 nm GaSb buffer layer
- 500 nm InAsSb bottom contact layer
- $\sim 1 \,\mu m$  GaSb buffer layer



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#### 1.55-µm GaSb laser diode on Si substrate

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#### InAs/AISb: very good intrinsic properties for QCLs

### **MBE growth**



#### **RIBER 412 MBE system**



#### Side-by-side growth on 2 substrates



H. Nguyen-Van et al., Sci. Rep. 8 (2018) 7206.

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### GaSb template grown on on-axis Si by MOVPE

# Remote plasma de-oxydation + high temperature annealing under H<sub>2</sub> flow in a Si cluster-tool



• 90° dislocations array at the interface

T. Cerba et al., Thin Solid Films 645 (2018) 5 (LTM, CNRS, CEA, University Grenoble Alpes)

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#### 2.3-µm GaSb laser on MOVPE-GaSb/Si template



Broad Area laser: J<sub>th</sub> = 0.9 A/cm<sup>2</sup> (1.5 mm) – 1.2 kA/cm<sup>2</sup> (2 mm)
Ridge (14 μm) laser: I<sub>th</sub> = 325 mA under **pulsed operation**

No cw operation (yet) Lower performances than on 6°off MBE templates Under investigation!

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### InAs/InAsSb type-II superlattice photodetector on MOVPE GaSb template





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#### Toward on-axis Si: an AFM view

#### Improved Si-surface preparation and growth start

#### 500 nm GaSb on Si



**APD free down to 0.5° miscut** 

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#### **GaSb-based lasers on Si: summary**

• Laser diodes grown on *off-cut* Si substrates

CW laser operation between 1.5 and 2.X  $\mu$ m  $T_{max} > 35 °C, T_0 \sim 50 K, P_{max} = 3 - 10 mW/uncoated facet$  $J_{th}(Si) \sim 3 - 5 x J_{th}(GaSb)$ 

• First-ever demonstration of QCL grown on *off-cut* Si substrates

InAs/AISb QCL design (vertical transition) for λ ~ 11 μm
Similar performances on Si and InAs substrates

Preliminary results on *on-axis* Si substrates (MOVPE template)

Pulsed operation of lasers diodes

InAs/InAsSb type-II superlattice photodetectors

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• Anti-phase-domain free GaSb MBE-templates

### GaSb-based lasers on Si: what's next?

- Devices on on-axis MBE templates
- Dislocation filtering
- Optimized device design and technology



**Development of a variety of Mid-IR integrated sensors** 

### Thank you for your attention!